

WHAT IS CLAIMED IS:

1. An information apparatus, comprising:

a first bus which transfers a non-encrypted transaction containing an address;

5 a second bus connected to an outside of the information apparatus; and

a bridge circuit connected between the first and second buses, the bridge circuit including a first controller which determines whether an address  
10 contained in the non-encrypted transaction transferred through the first bus falls within a first particular address range, and which prevents the non-encrypted transaction from being transmitted to the second bus, if the address falls within the first particular  
15 address range.

2. The information apparatus according to claim 1, wherein the bridge circuit further includes a second controller which determines whether an address contained in a transaction transferred through the  
20 second bus falls within a second particular address range, and which prevents the transaction from being transmitted to the first bus, if the address falls within the second particular address range.

3. The information apparatus according to claim 1, wherein the first controller of the bridge  
25 circuit includes:

a first storage which stores the address contained

in the transaction transferred through the first bus;

a second storage which stores the first particular address range;

5 a comparator which compares the address stored in the first storage with the first particular address range stored in the second storage; and

a process determination section which determines, from a comparison result of the comparator, whether the transaction transferred through the first bus should be transmitted to the second bus.

4. The information apparatus according to claim 3, wherein the first particular address range stored in the second storage corresponds to a particular address space on a predetermined memory.

15 5. The information apparatus according to claim 1, further comprising a slot to which an external device is attachable, the slot being connected to the second bus.

6. The information apparatus according to claim 1, wherein the first bus is a first peripheral component interconnect (PCI) bus and the second bus is a second PCI bus.

7. The information apparatus according to claim 6, further comprising a PCI slot to which a PCI device is attachable, the PCI slot being connected to the second PCI bus.

8. A transaction control method applied to

an information apparatus, comprising:

receiving a non-encrypted transaction transferred through a first bus, the non-encrypted transaction containing an address;

5 extracting the address from the received transaction; and

determining whether the extracted address falls within a particular address range, and preventing the transaction from being transmitted to a second bus connected to an outside of the information apparatus, if the extracted address falls within the particular address range.

9. The method according to claim 8, further comprising:

15 receiving a transaction transferred through the second bus, the transaction containing an address;

extracting the address from the received transaction; and

20 determining whether the extracted address falls within a particular address range, and preventing the transaction from being transmitted to the first bus if the extracted address falls within the particular address range.